

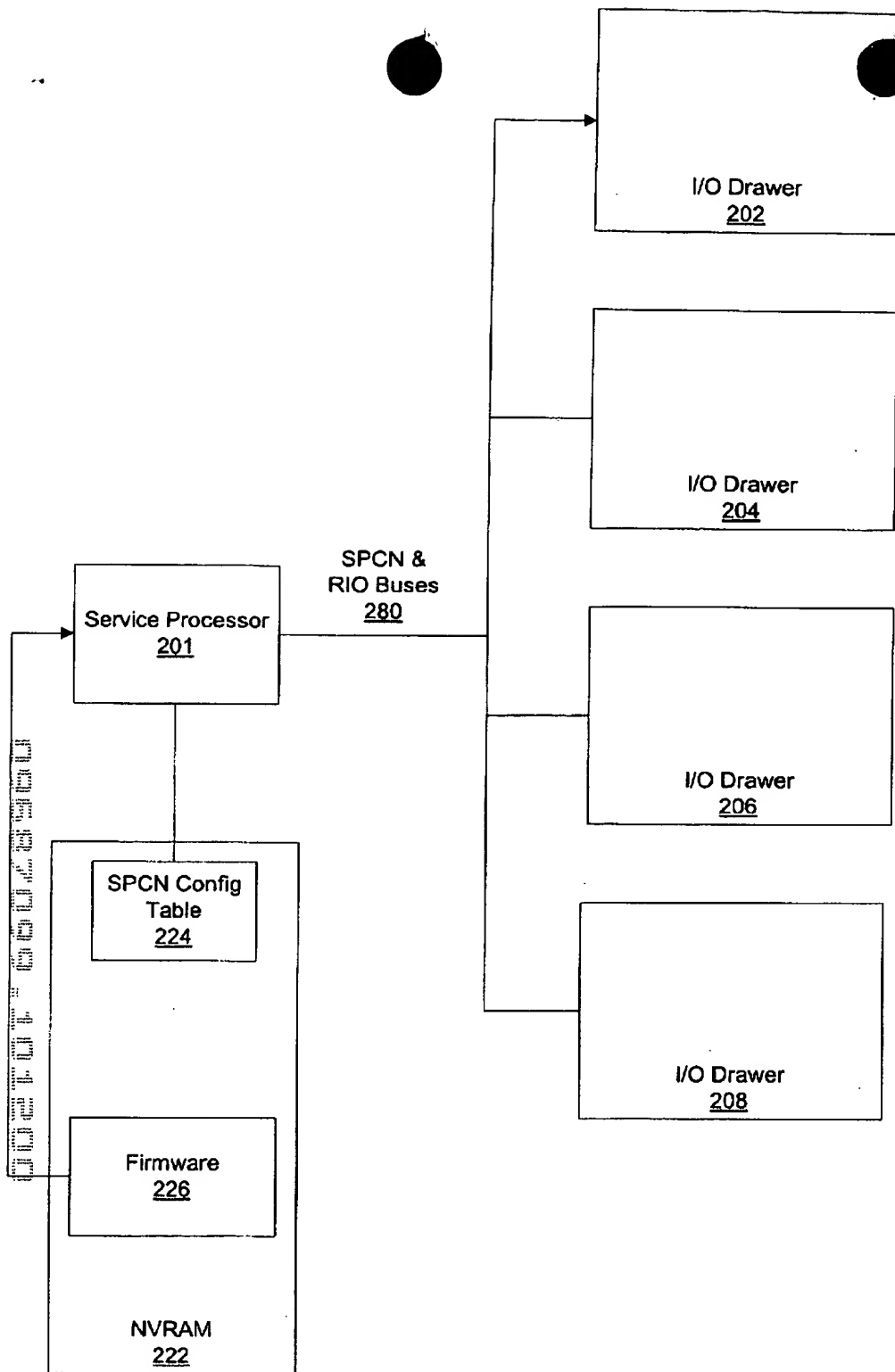
100

Data Processing System

Figure 1

AUS9-2000-0390-US1

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200
System

Figure 2

AUs9-2000-0390-US1

```

graph TD
    Start([Begin]) --> 302[Power on the system.  
302]
    302 --> 304[Service processor generates SPCN config table with all  
the drawers in the system and write to NVRAM.  
304]
    304 --> 306[Service processor ensures that each drawer connected  
via SPCN network has unique permanent address.  
306]
    306 --> 308[Firmware continues boot and reads SPCN config table  
information from NVRAM.  
308]
    308 --> 310[Firmware collects RIO network address for each drawer  
connected via RIO network.  
310]
    310 --> 312[Firmware fills up the SPCN config table with RIO  
network drawer address.  
312]
    312 --> 314[Firmware writes modified SPCN config table to NVRAM.  
314]
    314 --> 316{Firmware  
determines whether  
all drawer addresses match?  
316}
    316 -- YES --> End([End])
    316 -- NO --> 318[Firmware sends mailbox to service  
processor to assign new drawer address.  
318]
    318 --> 320[Service processor assigns new permanent  
SPCN drawer address to drawers that don't  
match with RIO drawer address.  
320]
    320 --> End
  
```

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